

CMPT215 Quiz 1

100
100

Consider two different implementations, M1 and M2, of the same instruction set. There are 3 different classes of instructions in the ISA. M1 has a clock rate of 800 MHz and M2 has a clock rate of 500 MHz. CPI and instruction mix is as follows.

Class	M1 CPI	M2 CPI	M1	M2
			C1 Usage (%)	C2 Usage (%)
A	4	1	30	70
B	6	4	50	20
C	8	6	20	10

C1 is a compiler produced by the manufacturers of M1, C2 is a compiler produced by the manufacturers of M2.

Assume that each compiler uses the same number of instructions for a given program but that the instruction mix is as described in the table above.

- Using C1 on both machines, compare the performance of the two machines. Which machine is faster, and by how much?
- Using C2 on both machines, compare the performance of the two machines. Which machine is faster, and by how much?
- If you could mix and match compilers and machines, what configuration would you choose? Assume that costs are identical.

$$1) ET_{M1} = \frac{d(4)(3) + d(6)(0.5) + d(8)(0.2)}{800(10^6)}$$

$$\frac{d(1.2) + d(3.0) + d(1.6)}{800(10^6)} = \frac{d(5.8)}{800(10^6)} \text{ sec}$$

$$ET_{M2} = \frac{d(0.3) + d(4)(0.5) + d(6)(0.2)}{500(10^6)} = \frac{d(3.5)}{500(10^6)} \text{ sec}$$

$$PC1(M2_{M1}) = \frac{3.5}{500(10^6)} \cdot \frac{800(10^6)}{5.8} = \frac{3.5(8)}{5.8} = \frac{28}{29}$$

should be M1/M2 so I flipped

* M2 is 29/28 times faster than M1 using C1.*

$$2) ET_{M1} = \frac{d(7)(1) + d(2)(6) + d(1)(8)}{800(10^6)} = \frac{d(2.8 + 1.2 + 0.8)}{800(10^6)} = \frac{d(4.8)}{800(10^6)} \text{ sec}$$

$$ET_{M2} = \frac{d(7)(1) + d(2)(4) + d(1)(6)}{500(10^6)} = \frac{d(0.7 + 0.8 + 0.6)}{500(10^6)} = \frac{d(2.1)}{500(10^6)} \text{ sec}$$

$$PC1 \frac{M1}{M2} = \frac{4.8}{2.1} = \frac{6.0}{2.1} = \frac{6.0}{2.1}$$

* M2 is 6.0/2.1 times faster than M1 using C2.*

over 2